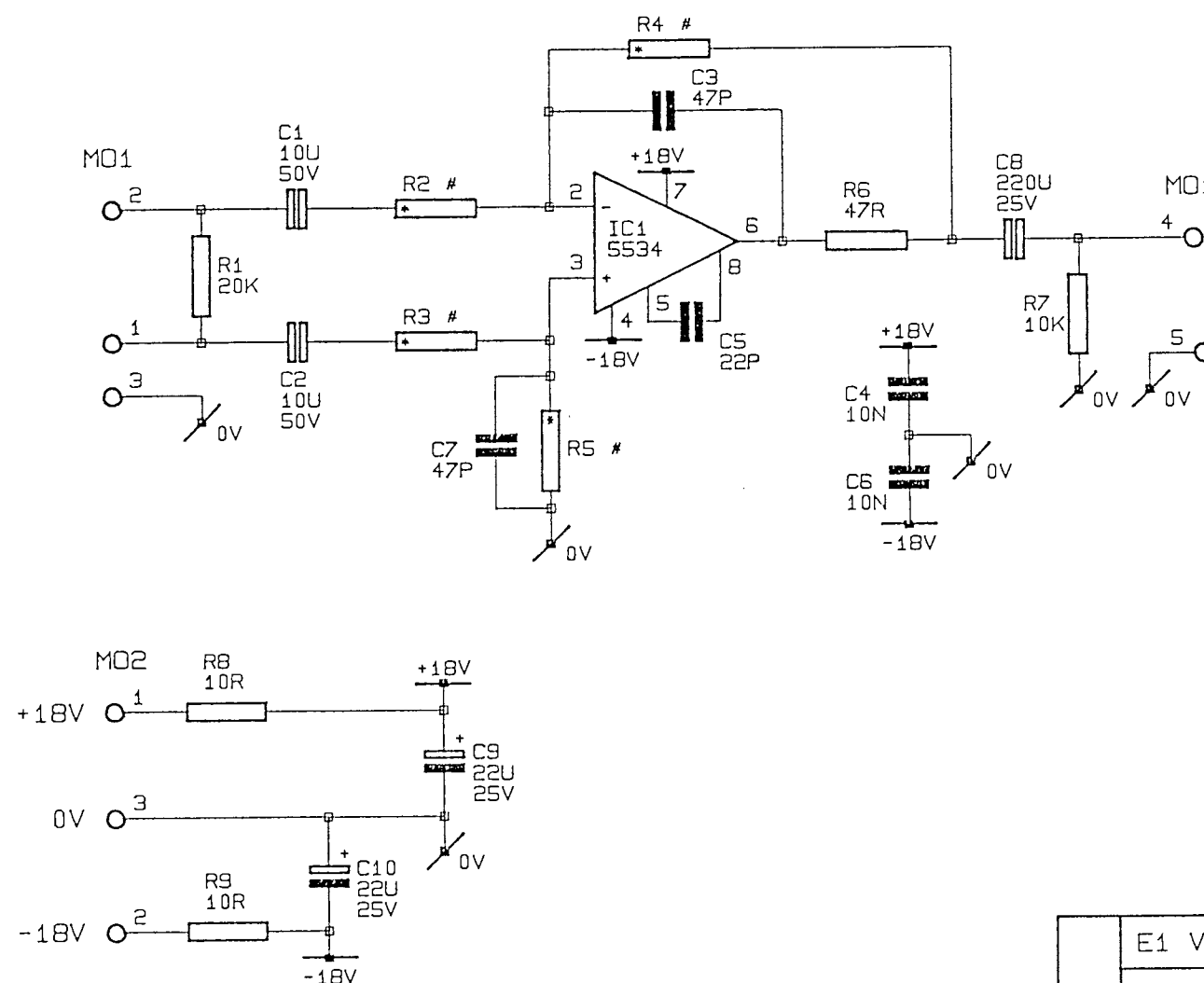


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REV	ISSUE	DATE	DETAILS
0	A	20 FEB 90	NEW DRAWING RP STEFF
0	B	5 MAR 90	C1:2 WERE 100U 16V NOW 10U 50V. R2-4 WERE 8K2 & 13K0 NOW 19K1 & 30K1



	E1 VERSION	E2 VERSION
	+4DB	-4DB
R2	19K1	30K1
R3	19K1	30K1
R4	30K1	19K1
R5	30K1	19K1

SHEET 1 OF 1

REF: T631B A3

TITLE 631
BALANCED INPUT BUFFER

DRG. NO. T82330.71

Solid State Logic